

JUL 07 2005

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SILICON VALLEY OFFICE

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To: USPTO
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From: Jeanette S. Harms, Esq.
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Email: jharms@beverlaw.com

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Pages: 6 (including Fax coversheet)

Phone: (703) 305-4958

Date: July 7, 2005

Re: US Serial No.: 10/003,358

CC:

Atty. Dkt. No.: NTI-022

Request Acknowledgement of IDS

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• **Comments:**

Following this transmission, please see the Information Disclosure Statement filed on December 6, 2002. An initialed acknowledgement has not been received for this IDS. Please provide an initialed acknowledgement via return facsimile. Thank you for your assistance with this matter.

Regards.

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Assistant Commissioner for Patents
Washington, D.C. 20231-0001

1st Inventor: Fang-Cheng Chang
Serial No.: 10/003,358-4650
Assignee: Numerical Technologies, Inc.
Title: "Simulation Using Design Geometry Information"

Dkt. No: NTI-022

Filed: 11/14/2001

ENCLOSED:

2 pages - Information Disclosure Statement Under 37 CFR 1.97
2 pages - Information Disclosure Statement (PTO/SB/08A)
1 copy of each cited reference (9)
return receipt Postcard

JSH :cr

Date Mailed: 12/6/2002

Assistant Commissioner for Patents
Washington, D.C. 20231-0001

1st Inventor: Fang-Cheng Chang
Serial No.: 10/003,358-4650
Assignee: Numerical Technologies, Inc.
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I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on December 6, 2002.

Signature: Carrie Reddick

PATENT

Attorney Docket No. NTI-022

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application)	<u>PATENT APPLICATION</u>
)	
Inventor(s): Fang-Cheng Chang)	
)	Art Unit: 2151
Application No.: 10/003,358)	
)	Examiner: unknown
Filed: 11/14/2001)	
)	
Title SIMULATION USING DESIGN GEOMETRY)	
INFORMATION)	
)	
)	

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Listed below or on an attached Form PTO-1449 is information known to applicant(s). A copy of each listed publication and U.S. and foreign patent, except for pending U.S. applications, is being submitted herewith, along with a concise explanation of information in a foreign language, if any, pursuant to 37 C.F.R. §1.97-1.98.

Applicants respectfully request that the listed information be considered by the Examiner and be made of record in the above-identified application. If form PTO-1449 is enclosed, the Examiner is requested to initial and return it in accordance with MPEP § 609.

This statement is not intended to represent that a search has been made or that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56.

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SN: 10/003,358

- ☒ This statement qualifies under 37 C.F.R. § 1.97, subsection (b) because (check all that apply):
- ☐ (1) It is being filed within 3 months of the application filing date and is other than a continued prosecution application under § 1.53(d)
-- OR --
- ☐ (2) It is being filed within 3 months of entry of a national stage
-- OR --
- ☒ (3) It is being filed before the mail date of the first Office Action on the merits.
-- OR --
- ☐ (4) It is being filed before the mailing of a first Office Action after the filing of a request for continued examination under § 1.114
- ☐ 37 C.F.R. § 1.97(c). If this statement is being filed after the period specified in § 1.97(b), but before the mailing date of the earlier of a final office action under § 1.113, a notice of allowance under § 1.311, or an action that otherwise closes prosecution in the application, then:
- ☐ a certification as specified in § 1.97(e) is provided below; or
- ☐ a fee of \$180.00 as set forth in § 1.17(p) is authorized below, enclosed, or included with the payment of other papers filed together with this statement.
- ☐ 37 C.F.R. § 1.97(d). If this statement is being filed after the period specified in § 1.97(c), but on or before payment of the issue fee, then:
- A. a certification as specified in § 1.97(e) is completed below; and
- B. a fee of \$180.00 as set forth in § 1.17(p) is authorized below, enclosed, or included with the payment of other papers filed together with this statement.
- ☒ **Fee Authorization.** Applicant believes no fee is due. However, if a fee is found to be owing, the Commissioner is hereby authorized to charge the fee of \$180.00 and charge any additional fees or credit any overpayment associated with this communication to Deposit Account No. 50-0574 (Docket No. NTI-022).

Respectfully submitted,

BEVER, HOFFMAN & HARMS, LLP

Dated: December 6, 2002

By:

Jeanette S. Harms, Reg. No. 35,537

Telephone: (408) 451-5907
Customer No. 29477

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SHEET 1 of 2

INFORMATION DISCLOSURE CITATION			Atty. Docket No. NTI-022				Serial No. 10/003,358	
PTO-1449			Applicant CHANG, Fang-Cheng				Group 2151	
Filing Date 11/14/2001								
U.S. PATENT DOCUMENTS								
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE		
	5,282,140	1/25/1994	Tazawa, et al.	364	468	6/24/1992		
	6,130,750	10/10/2000	Ausschnitt, et al.	356	401	8/28/1997		
	6,225,025 B1	5/1/2001	Hoshino	430	296	9/22/1998		
	6,339,836 B1	1/15/2002	Eisenhofer, et al.	716	5	8/24/1998		
	6,346,426 B1	2/12/2002	Toprac, et al.	438	8	11/17/2000		

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Date Considered: _____

EXAMINER: Initial if reference considered, whether or not citation is in conformance with
MPEP § 609; draw line through citation if not in conformance and not considered. Include
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INFORMATION DISCLOSURE CITATION PTO-1449	Atty. Docket No. NTI-022	Serial No. 10/003,358
	Applicant CHANG, Fang-Cheng	
	Filing Date 11/14/2001	Group 2151
	OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)	
EXAMINER'S INITIALS	CITATION	
	Lin, B.J., et al., "Single-Level Electric Testsites for Phase-Shifting Masks", SPIE, Vol. 1673, pp. 221-228, March 9-11, 1992.	
	Cobb, N., "Fast Optical and Process Proximity Correction Algorithms for Integrated Circuit Manufacturing", Dissertation, University of California at Berkeley, UMI Microform 9902038 (139 pages).	
	Uhring, W., et al., "Model of an Instrumented Opto-Electronic Transmission System in HDL-A and VHDL-AMS", SPIE, Vol. 3893, pp. 137-146, October 1999.	
	Gratik, Y., et al., "Sub-Resolution Process Windows And Yield Estimation Technique Based On Detailed Full-Chip CD Simulation", SPIE, Vol. 4182, pp. 335-341 (2000).	

EXAMINER: _____

Date Considered: _____

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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